


## Nets with Only One Pin

Modified by Jason Howie on Oct 20, 2014

Parent category: [Violations Associated with Nets](#)

Default report mode:  No Report

### Summary

This violation occurs when a net in the design has been detected to contain only one component pin.

### Notification

If compiler errors and warnings are enabled for display on the schematic (enabled on the **Schematic - Compiler** page of the *Preferences* dialog) an offending object will display a colored squiggle beneath it. A notification is also displayed in the *Messages* panel in the following format:

Net <NetName> has only one pin (Pin <PinName>),

where:

*NetName* is the name of the parent net.

*PinName* is the component designator-pin designator identifier.

### Recommendation for Resolution

By default, this message will not appear in the *Messages* panel. If you have enabled reporting for this violation type and want to resolve the issue, simply ensure that the offending net is wired to at least two component pins in the design.

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**Source URL:**

[http://documentation.circuitmaker.com/display/CMAK/WorkspaceManager\\_Err-NetsWithOnlyOnePin\(\(Nets+with+Only+One+Pin\)\)\\_CM](http://documentation.circuitmaker.com/display/CMAK/WorkspaceManager_Err-NetsWithOnlyOnePin((Nets+with+Only+One+Pin))_CM)