

Bus Range Syntax Errors

Modified by Jason Howie on Oct 20, 2014

Parent category: [Violations Associated with Buses](#)

Default report mode:  Error

Summary

This violation occurs when the syntax of the net to which the bus is associated is specified incorrectly.

Notification

If compiler errors and warnings are enabled for display on the schematic (enabled on the **Schematic - Compiler** page of the *Preferences* dialog) an offending object will display a colored squiggle beneath it. A notification is also displayed in the *Messages* panel in the following format:

Bus range syntax error *<NetName>* at *<Location>*,

where:

NetName is the name of the parent net to which the offending bus object is associated.

Location is the X, Y coordinates for the offending bus object's electrical hotspot.

Recommendation for Resolution

With the violation selected in the *Messages* panel, use the **Details** region of the panel to quickly cross probe to the offending net identifier (e.g. net label, port, sheet entry, etc) whose bus syntax is defined incorrectly. The correct syntax should appear in one of the following formats:

- `NetName[LowerIndex..UpperIndex]`
- `NetName[UpperIndex..LowerIndex]`

For example, consider a bus that carries two constituent nets, A0 and A1. The bus syntax in this case should be `A[0..1]` or `A[1..0]`. Examples of incorrect syntax would include: `A[0.1]`, `A[1-0]`, `A[0,1]`, `A[..1]`, and `A[0..]`.

Source URL:

[http://documentation.circuitmaker.com/display/CMAK/WorkspaceManager_Err-BusRangeSyntaxErrors\(\(Bus+Range+Syntax+Errors\)\)_CM](http://documentation.circuitmaker.com/display/CMAK/WorkspaceManager_Err-BusRangeSyntaxErrors((Bus+Range+Syntax+Errors))_CM)